| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 3131051 | memory or storage | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/06/07 13:36 |
| L2 | 1909 | 1 and (monitor$5 or detect$5) with (bit or bitline) near3 current | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/06/07 14:43 |
| L3 | 3 | 2 and compensat$5 with (voltage or potential or level) near3 drop | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/06/07 14:01 |
| L5 | 133 | 2 and (voltage or potential or level) near3 drop with (switch or transistor) | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/06/07 14:02 |
| L6 | 46 | 5 and compensat$5 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/06/07 14:02 |
| L7 | 57 | 2 and (switch or transistor) near3 drop | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/06/07 14:54 |
| L8 | 12 | 2 and (switch or transistor) near3 drop and compensat$5 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2005/06/07 14:54 |